

Application No. 10/061,949  
Reply to Office Action dated November 4, 2004

**Amendments to the Specification:**

Please replace the paragraph beginning at page 6, line 3, with the following redlined paragraph:

The logic ~~signal~~-signals tst\_dcpathoff<sub>1</sub> and tst\_dcpathoff<sub>2</sub> present on the ~~line~~-lines PD1, and PD2 ~~acts~~act, via a respective inverter 60, which also has the function of impedance separator, so as to isolate or apply the corresponding clock on the line 50.